S/N 09/347,690

**PATENT** 

IN THE SNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:

Manpreet S. Khaira et al.

Examiner: Dwin M. Craig

Serial No.:

09/347,690

Group Art Unit: 2123

Filed:

July 2, 1999

Docket: 884.107US1

Title:

LOGIC VERIFICATION IN LARGE SYSTEMS

Assignee:

Intel Corporation

Customer No. 21186

## NOTICE OF APPEAL FROM THE DECISION OF THE EXAMINER TO THE BOARD OF PATENT APPEALS AND INTERFERENCES

## **MAIL STOP AF**

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

In compliance with 37 C.F.R. § 41.31(a)(1), Applicants hereby appeal to the Board of Patent Appeals and Interferences from the decision dated November 3, 2004, of the Examiner rejecting claims 1, 2, 10, 14, 16-18 and 21 of the above-identified patent application.

A request for an extension of time to respond to the Examiner's rejection is submitted herewith along with payment of the required extension fee.

Please charge the amount of \$500.00 in payment of the Notice of Appeal fee under 37 C.F.R. § 41.20(b)(1), as well as any additional required fees, to Deposit Account No. 19-0743.

Respectfully submitted,

MANPREET S. KHAIRA ET AL.

By their Attorneys,

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Rv

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CERTIFICATE UNDER 37 C.F.R. 1.8:

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to MAIL STOP AF, Commissioner of Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on May 3 2005.

Name

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